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## Chapter (1)

# Physics of MOSFET Transistor

Problems

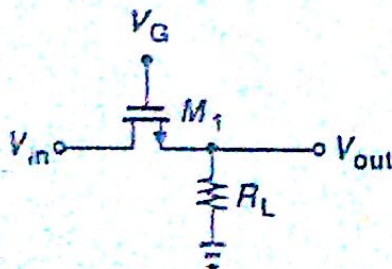
(( اللهم رحمتك أرجو ، فلا تكلني إلى نفسي طرفة عين ، وأصلح لي شأني كله ، لا إله إلا أنت ))



## Problems

In the following problems, unless otherwise stated, assume  $\mu_n C_{ox} = 200 \mu A / V^2$ ,  $\mu_p C_{ox} = 100 \mu A / V^2$ , and  $V_{TH} = 0.4 V$  for NMOS devices and  $-0.4 V$  for PMOS devices.

1. Calculate the total charge stored in the channel of an NMOS device if  $C_{ox} = 10 \text{ fF} / \mu m^2$ ,  $W = 5 \mu m$ ,  $L = 0.1 \mu m$ , and  $V_{GS} - V_{TH} = 1 V$ . Assume  $V_{DS} = 0$ .
2. An NMOS device carries  $1 \text{ mA}$  with  $V_{GS} - V_{TH} = 0.6 V$  and  $1.6 \text{ mA}$  with  $V_{GS} - V_{TH} = 0.8 V$ . If the device operates in the triode region, calculate  $V_{DS}$  and  $W / L$ .
3. An NMOS device operating with a small drain-source voltage serves as a resistor. If the supply voltage is  $1.8 V$ , what is the minimum on-resistance that can be achieved with  $W / L = 20$ ?
4. We wish to use an NMOS transistor as a variable resistor with  $R_{on} = 500 \Omega$  at  $V_{GS} = 1 V$  and  $R_{on} = 400 \Omega$  at  $V_{GS} = 1.5 V$ . Explain why this is not possible.
5. In the circuit in Fig.1, shown,  $M_1$  serves as an electronic switch. If  $V_{in} \sim 0$ , determine  $W / L$  such that the circuit attenuates the signal by only 5%. Assume  $V_G = 1.8 V$  and  $R_L = 100 \Omega$ .

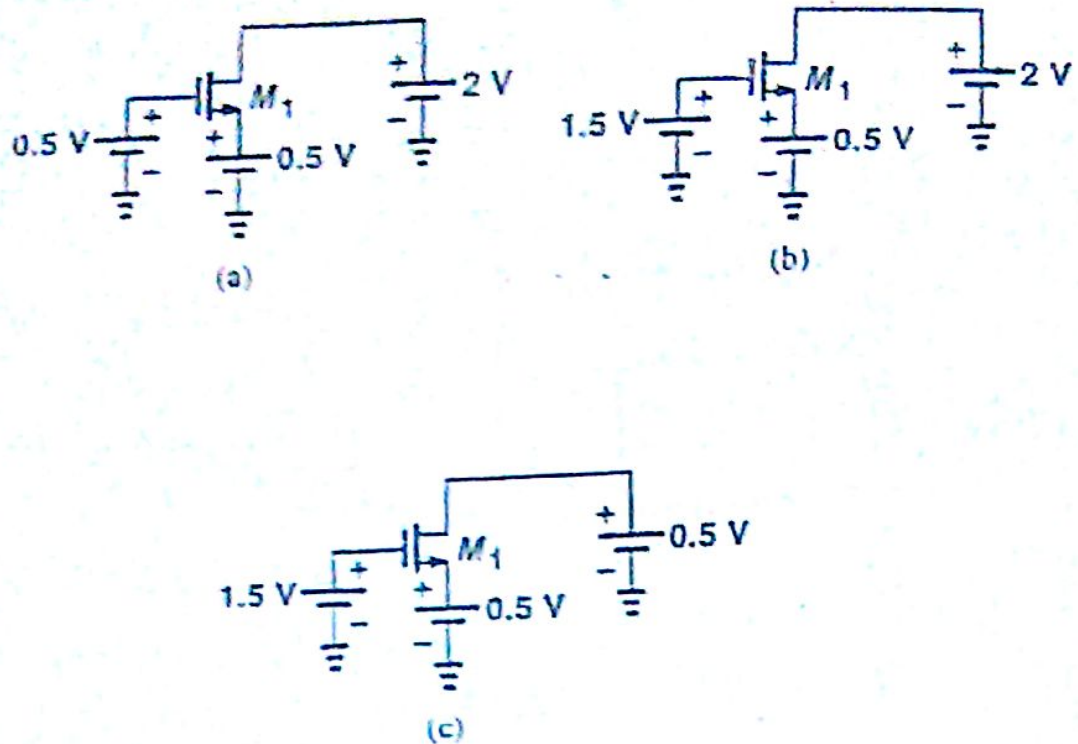




- ✓ 6. Determine the region of operation of  $M_1$  in each of the circuit shown in Fig.2

Fig.2

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- ✓ 7. Determine the region of operation of  $M_1$  in each of the circuit shown in Fig.3

Fig.3



(ii) Total charge stored in channel = ? (1)

$$C_{ox} = 10 \text{ fF}/\mu\text{m}^2, W = 5 \mu\text{m}, L = 0.1 \mu\text{m}$$

$$V_{GS} - V_{TH} = 1 \text{ V. assume } V_{DS} = 0.$$

Solution:-

$$\phi(x) = W C_{ox} [V_{GS} - V(x) - V_{TH}]$$

$$\because V_{DS} = 0$$

$$\because V(x) = 0.$$

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$$\therefore \phi(x) = W C_{ox} [V_{GS} - V_{TH}].$$

$$\therefore \phi(x) = 5 \times 10^{-6} \times 10 \times 10^{-15} \times 10^{+12} [1.]$$

$$\therefore \phi(x) = 5 \times 10^{-8} \text{ e/m.}$$

↓

$$\text{channel charge density} = \frac{\text{charge}}{\text{length.}}$$

$$\therefore \text{Total charge} = \phi(x) \times L$$

$$\therefore \text{Total charge} = 5 \times 10^{-8} \times 0.1 \times 10^{-6}$$

$$= 5 \times 10^{-15} \text{ electrons.}$$



(2)  $I_D = 1 \text{ mA} \rightarrow (V_{GS} - V_{TH}) = 0.6 \text{ V}$

$I_D = 1.6 \text{ mA} \rightarrow (V_{GS} - V_{TH}) = 0.8 \text{ V}$

if MOSFET in Triode region

Calculate  $V_{DS}$  and  $W/L$ .

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Solution:

∴ in Triode regions.

$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} [2(V_{GS} - V_{TH})V_{DS} - V_{DS}^2]$$

$$\therefore 1 \text{ mA} = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} [2 \times 0.6 \times V_{DS} - V_{DS}^2] \rightarrow (1)$$

$$1.6 \text{ mA} = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} [2 \times 0.8 \times V_{DS} - V_{DS}^2] \rightarrow (2)$$

(1) على (2) نقسم

$$\therefore \frac{1}{1.6} = \frac{1.2 V_{DS} - V_{DS}^2}{1.6 V_{DS} - V_{DS}^2}$$

$$\therefore 0.6 V_{DS}^2 - 0.32 V_{DS} = 0$$

$$\therefore V_{DS} [0.6 V_{DS} - 0.32] = 0$$

$$\therefore V_{DS} = 0 \quad V_{DS} = \frac{0.32}{0.6} = 0.533$$



$$V_{DS} = 0 \longrightarrow \text{مرحله MP}$$

(3)

$$\therefore V_{DS} = 0.533 \text{ V}$$

بالقوة من الجهد

$$\therefore 1 \text{ mA} = \frac{1}{2} \times 200 \times 10^{-6} \times \frac{W}{L} [2 \times 0.6 \times 0.533 - 0.533^2]$$

$$\therefore W/L = 28.1$$

(3) NMOSFET  $\longrightarrow$  resistor

$$\text{Supply voltage} = 1.8 \text{ V}$$

What is the minimum on-resistor = ?

$$W/L = 20$$

Solution:-

$$R_{on} = \frac{1}{\mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})}$$

$$\therefore R_{on} \downarrow V_{GS} \uparrow = 1.8 \text{ V.}$$

$$\begin{aligned} R_{on} &= \frac{1}{200 \times 10^{-6} \times 20 \times (1.8 - 0.4)} \\ &= 178.57 \Omega. \end{aligned}$$



## N MOSFET

$$R_{on} = 500 \, \Omega \longrightarrow V_{GS} = 1 \, V$$

$$R_{on} = 400 \, \Omega \longrightarrow V_{GS} = 1.5 \, V$$

Explain why this is not possible.

Solution:

$$\therefore R_{on} = \frac{1}{\mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})}$$

$$\therefore W/L = \frac{1}{\mu_n C_{ox} R_{on} (V_{GS} - V_{TH})}$$

When  $R_{on} = 500 \, \Omega$

$$\therefore W/L = \frac{1}{200 \times 10^{-6} \times 500 \times (1 - 0.4)}$$

$$\therefore W/L = 16.7 \longrightarrow (1)$$

When  $R_{on} = 400 \, \Omega$

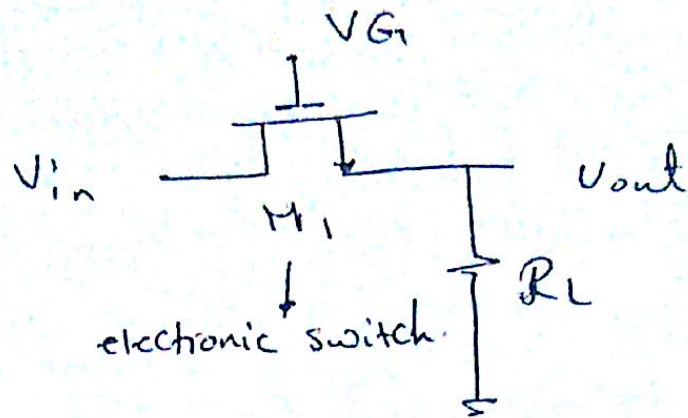
$$\therefore W/L = \frac{1}{200 \times 10^{-6} \times 400 (1.5 - 0.4)}$$

$$\therefore W/L = 11.36 \longrightarrow (2)$$



5) Determine  $W/L$  ratio of the MOSFET,  $V_{GS} = 1.8V$ ,  $R_L = 100\Omega$ ,  $V_{in} = 1.8V$ ,  $V_{out} = 0.95V_{in}$ .

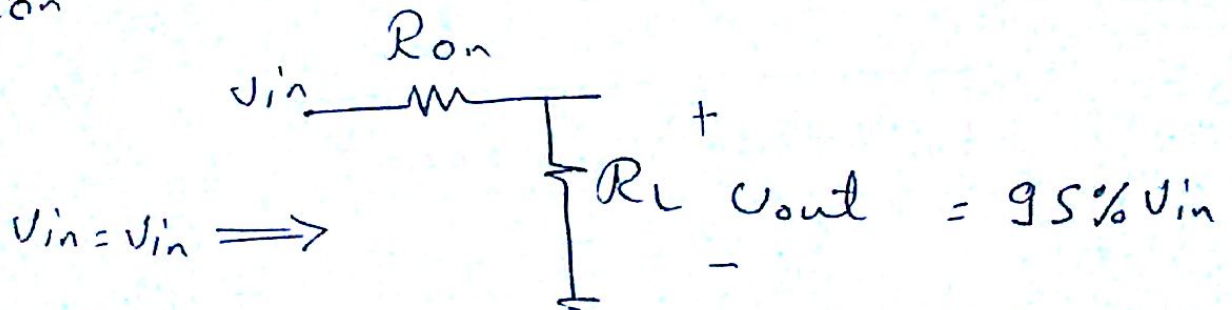
(5)



determine  $W/L = ?$ . the circuit attenuates

the signal by only 5%,  $V_{GS} = 1.8V$ ,  $R_L = 100\Omega$ .

Solution



$$\therefore V_{out} = 0.95 V_{in}$$

$$\therefore \frac{V_{out}}{V_{in}} = 0.95 \longrightarrow (1)$$

$$\therefore V_{out} = V_{in} * \frac{R_L}{R_L + R_{on}}$$

$$\therefore \frac{V_{out}}{V_{in}} = \frac{R_L}{R_L + R_{on}} \longrightarrow (2)$$



(6)

from (1) &amp; (2)

$$\therefore \frac{R_L}{R_L + R_{on}} = 0.95$$

$$\therefore R_L = 0.95 R_L + 0.95 R_{on}$$

$$\therefore R_{on} = \frac{0.05 R_L}{0.95} = \frac{0.05 \times 100}{0.95} = 5.26 \Omega$$

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$$\therefore R_{on} = \frac{1}{\mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})}$$

$$\therefore W/L = \frac{1}{\mu_n C_{ox} R_{on} (V_{GS} - V_{TH})}$$

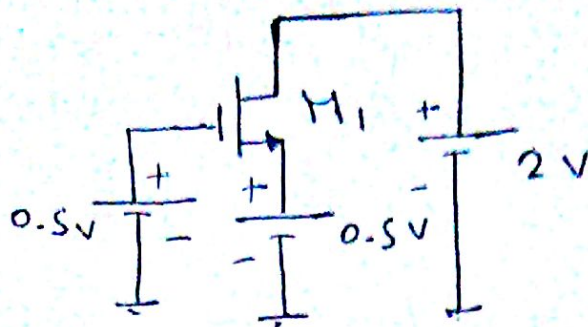
$$\therefore W/L = \frac{1}{200 \times 10^{-6} \times 5.26 (1.8 - 0.4)}$$

$$\therefore W/L = 678.9$$



Determine the region of operation? (7)

(a)



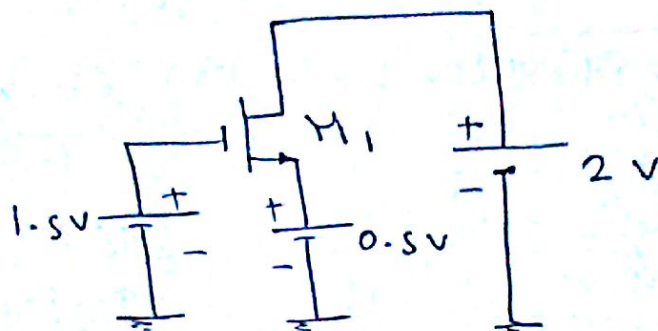
$$V_{GS} = 0 < V_{TH} (0.4V) \therefore \text{No channel}$$

$$\therefore I_D = 0$$

$\therefore M_1 \rightarrow$  cutoff region.

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(b)



$$V_{GS} = V_G - V_S = 1.5 - 0.5 = 1V > V_{TH}$$

$$V_{GD} = V_G - V_D = 1.5 - 2 = -0.5V$$

$$V_{GD} < V_{TH} (0.4V)$$

المصدر الثاني

$$V_{DS} = V_D - V_S = 2 - 0.5 = 1.5V$$

$$V_{GS} - V_{TH} = 1 - 0.4 = 0.6V$$

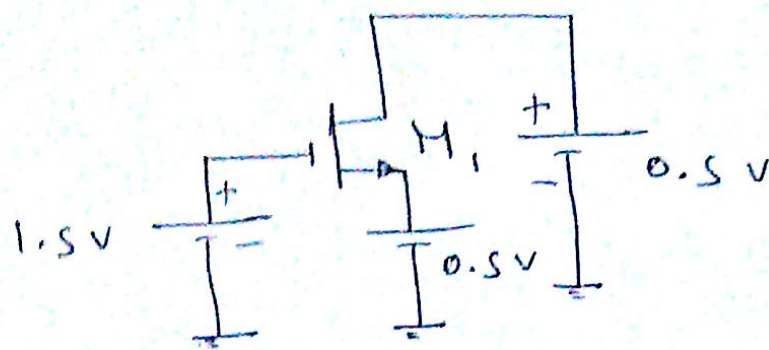
$\therefore V_{DS} > V_{GS} - V_{TH} \therefore M_1 \rightarrow$  saturation region

$\therefore$  saturation



(6)

(8)



$V_{DS} = 0V$   
 $\therefore I_D = 0$   
 $\therefore M_1 \rightarrow$  Cut off

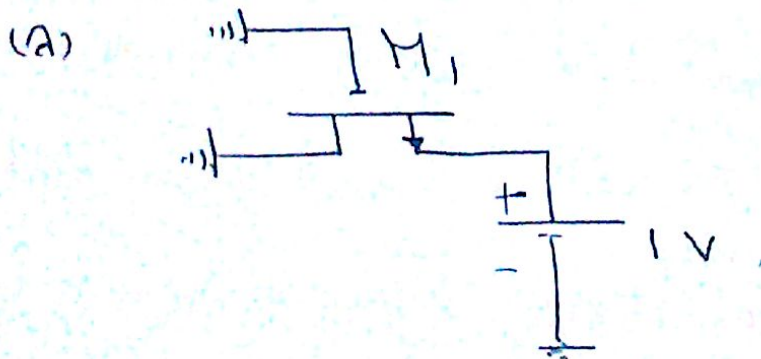
$$V_{GS} = 1.5 - 0.5 = 1V > V_{TH}^{0.4V}$$

$$V_{GD} = V_G - V_D = 1.5 - 0.5 = 1V > V_{TH}^{0.4V}$$

$$\text{but } V_{DS} = 0 \therefore I_D = 0$$

$\therefore M_1 \rightarrow$  Cut off region

(7) Determine the region of operation?



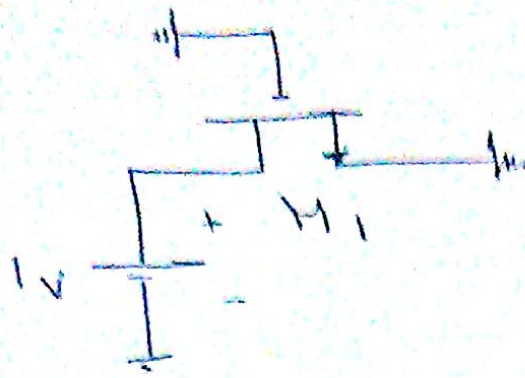
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$$V_{GS} = V_G - V_S = 0 - 1 = -1 < V_{TH}^{0.4V}$$

$\therefore M_1 \rightarrow$  Cut off



(b)



(9)

$$V_{GS} = V_G - V_S = 0 - 0 = 0 < V_{TH} \quad 0.4V$$

$\therefore M_1 \rightarrow \text{Cutoff.}$

ملاحظة!

(8) it is possible to define an "intrinsic

Time Constant" for a MOSFET operating as a register

$$\tau = R_{on} C_{GS}$$

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Where  $C_{GS} = WL C_{ox}$

obtain an expression for  $\tau$  and explain what is the circuit designer must do to minimize the Time Constant.

Solution

$$\therefore \tau = R_{on} C_{GS}$$

$$R_{on} = \frac{1}{\mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})}$$



(10)

$$C_{GS} = W L C_x$$

$$\therefore \tau = \frac{1}{\mu_n C_x \frac{W}{L} (V_{GS} - V_{TH})} \times \cancel{W L C_x}$$

$$\therefore \tau = \frac{L^2}{\mu_n (V_{GS} - V_{TH})}$$

To minimize the time constant.

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\* Use minimum  $L$

\* Maximized overdrive voltage  
 $\downarrow$   
 $(V_{GS} - V_{TH})$

Good Luck